Internship within Airbus: Implementation of radar functions in FPGA

Are you looking for an internship? Would you like to discover the work of an Engineer? Then apply now! We look forward to you joining us as an intern (d/f/m) (35 hours/week, flexi-time).

Location: Friedrichshafen / Start: April

Description of the department: Development of future spaceborne Radar Systems and Subsystems for diverse customers (ESA, DLR).
Upcoming spaceborne radar instruments will be equipped with powerful processing modules and devices for data acquisition and data processing. Radar functions, device controllers or signal processing modules are to be implemented in a FPGA test system.

As an intern you will learn about: radar instruments, space electronics, digital data processing for radar, VHDL design for FPGAs, Satellite development

Your Tasks:
• VHDL design and simulation
• FPGA programming
• Design verification on hardware

You offer:
• Enrolled student within electrical engineering, computer science or similar field of study
• Experience in VHDL programming
• Experience in digital signal processing
• Knowledge about embedded systems and processor systems
• English and/or German

You are a good team player, have excellent communication skills, and are able to work independently.

Does this job description fit your objectives and profile? Take the next step in your career and come and join us!

How to apply:
Online via www.airbus.com / Reference Code: JR10023401

Please provide the following documents: cover letter, C.V., relevant certificates, current certificate of enrolment, excerpt from your study regulations/guidelines if it is a compulsory internship)

Should you have general questions regarding this position you can write an E-Mail to: stefanie.seeger@airbus.com

To apply our passion for aerospace to create a better connected, safer and more prosperous world.